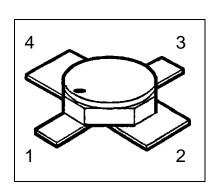


# HiRel NPN Silicon Germanium RF Transistor

- HiRel Discrete and Microwave Semiconductor
- High gain low noise RF transistor
- High maximum stable gain: G<sub>ms</sub> 24dB at 1.8 GHz
- Noise figure F = 0.8 dB at 1.8 GHz
   Noise figure F = 1.1 dB at 6 GHz
- Hermetically sealed microwave package

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**ESD**: Electrostatic discharge sensitive device, observe handling precautions!

Туре	Marking	Pin Configuration				Package
		1	2	3	4	
BFY640-04	-	С	Е	В	E	Micro-X

## **Maximum Ratings**

Parameter	Symbol	Values	Unit
Collector-emitter voltage $T_a > 0 \text{ °C}$ $T_a \le 0 \text{ °C}$	V <sub>CEO</sub>	4.0 3.7	V
Collector-base voltage	V <sub>CBO</sub>	13	V
Emitter-base voltage	V <sub>EBO</sub>	1.2	V
Collector current 1)	I <sub>C</sub>	50	mA
Base current	I <sub>B</sub>	3	mA
Junction temperature	T <sub>j</sub>	175	°C
Operating temperature range	T <sub>op</sub>	-65+175	°C
Storage temperature range	T <sub>stg</sub>	-65+175	°C
Thermal Resistance		•	1
Junction-soldering point 2)	R <sub>th JS</sub>	325	K/W

## Notes.:

<sup>1)</sup> For  $T_S \le 110^{\circ}$ C. For  $T_S > 110^{\circ}$ C derating is required.

<sup>2)</sup>  $T_{\mbox{\scriptsize S}}$  is measured on the emitter lead at the soldering point to the pcb.



# **Electrical Characteristics**

at T<sub>A</sub>=25°C; unless otherwise specified

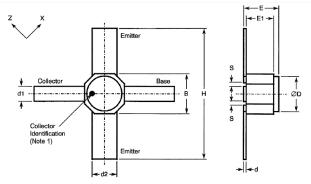
Parameter	Symbol		Values		Unit
		min.	typ.	max.	
DC Characteristics				•	
Collector-base cutoff current	I <sub>CBO</sub>	-	-	10	μΑ
$V_{CB} = 5 \text{ V}, I_E = 0$					
Collector-emitter cutoff current 1)	I <sub>CEX</sub>	-	-	200	μΑ
$V_{CE}=4.0~V,~I_{B}=0.1~\mu A$					
Emitter-base cuttoff current	I <sub>EBO</sub>	-	-	5	μΑ
$V_{EB} = 1.2 \text{ V}, I_{C} = 0$					
DC current gain	h <sub>FE</sub>	135	180	250	-
$I_C = 30$ mA, $V_{CE} = 3$ V					
AC Characteristics			•	•	
Collector-base capacitance	C <sub>CB</sub>	-	0.07	-	pF
$V_{CB} = 2 \text{ V}, V_{BE} = \text{vbe} = 0, f = 1 \text{ MHz}$					
Collector-emitter capacitance	C <sub>CE</sub>	-	0.45	-	pF
$V_{CE} = 2 \text{ V}, V_{BE} = \text{vbe} = 0, f = 1 \text{ MHz}$					
Emitter-base capacitance	C <sub>EB</sub>	-	0.6	-	pF
$V_{EB} = 0.5V$ , $V_{CB} = vcb = 0$ , $f = 1 MHz$					
Noise Figure ( $Z_S = Z_{sopt}$ )	F				dB
$I_C = 5$ mA, $V_{CE} = 3$ V, $f = 1.8$ GHz		-	0.8	-	
$I_C = 5 \text{ mA}, V_{CE} = 3 \text{ V}, f = 6.0 \text{ GHz}$	12 12	-	1.1	-	
Insertion power gain ( $Z_S = Z_L = 50 \Omega$ )	$\left S_{21e}\right ^2$				dB
$I_{C} = 30$ mA, $V_{CE} = 3$ V, $f = 1.8$ GHz $I_{C} = 30$ mA, $V_{CE} = 3$ V, $f = 6.0$ GHz		-	22.5 12.5	-	
Power gain ( $Z_S = Z_{Sopt}$ , $Z_L = Z_{Lopt}$ )	G <sub>ms</sub> <sup>2)</sup>				dB
$I_C = 30 \text{ mA}, V_{CE} = 3 \text{ V}, f = 1.8 \text{ GHz}$		-	24	-	
Power gain $(Z_S = Z_{Sopt}, Z_L = Z_{Lopt})$	G <sub>ma</sub> <sup>2)</sup>				dB
$I_C = 30 \text{ mA}, V_{CE} = 3 \text{ V}, f = 6.0 \text{ GHz}$		-	14	-	

Notes.:
1) This Test assures V(BR)CE0 > 4.0V

2) 
$$G_{ma} = \left| \frac{S21}{S12} \right| (k - \sqrt{k^2 - 1}), \quad G_{ms} = \left| \frac{S21}{S12} \right|$$



# Micro-X Package



Symbols	Dimensions mm			
	Min	Max		
В	1.68	1.88		
d	0.07	0.15		
d1	0.4	0.6		
d2	0.92	1.12		
ØD	1.55	1.85		
E	0.85	1.25		
E1	0.66	0.86		
Н	4	4.4		
S	0.08	0.3		

Edition 2016-06
Published by
Infineon Technologies AG
85579 Neubiberg, Germany
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